

## ABSTRACT OF THE DISCLOSURE

In accordance with a circuit modification method, when it is determined that an aggressor (2) causes a glitch error in a victim (1), the one or more positions where one or more buffers are to be inserted into the victim (1) are determined based on the coupling capacity  $C_c$  between the victim (1) and the aggressor (2). One or more buffers are inserted at one or more internal points of division of the victim (1) which are determined so that the coupling capacity between each of a plurality of wire segments, into which the victim is to be divided by the one or more internal points of division, and the aggressor 2 is equal to  $C_c/n$ , where  $n$  is the number of wire segments. Furthermore, since the one or more buffers are inserted so that the coupling capacity  $C_c$  is properly divided into the coupling capacities between the plurality of wire segments and the aggressor, the amount of glitch to be caused in each of plurality of wire segment can be reduced and no further addition of buffers is needed.

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